

## BEST Interface

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# Chapter 1

## libbest

Library to interface with BEST Central Unit FPGA.

### 1.0.1 changelog version

The last version in `debian/changelog` should be equal to the one in files `version` and in `build_number`. After succesfull compilation the `build_number` and the updated `debian/changelog` should be checked in the version control.

## 1.1 Before building

- update `.version` and `.buildnumber`
- update debian files
  - update changelog with `dch -i` accordingly to version and buildnumber
  - check `debian/control` file (if changes required)
  - update [Readme.md](#) -> Changelog and FunctionList
- commit the changes
- `make publish`
- `doxy Doxyfile` to update the documentation

## 1.2 Generated files

Generated `.deb` files are stored in `pkg` folder.

### 1.2.1 Build issues:

```
sudo apt install libfftw3-dev
```

## 1.3 TEST

A python script (scanAllGetFunctions.py) can be used to have an idea of who is changing the BEST internal variables

## 1.4 Changelog

### 1.4.1 1.8-19

- Added `setSetpointEnbox`, `getSetpointEnbox` ONLY for Enbox the setpoint is not scaled by scaling parameters

### 1.4.2 1.8-18

- Fixed `setBPMoffsetEnbox` compatible with firmware 1.2.20
- Added `setBPMdevice`, `setEnboxType`, `setEnboxSumDiff` to FPGA for Difference Over Sum Calculations
- Removed `enboxMathSelect`

### 1.4.3 1.8-17

- Added additional channel for `func_gen()` for frequency analysis with PreDAC (before was used the 4-th channel of the predac internally)

### 1.4.4 1.8-16

- Added `setBPMoffsetEnbox()`: to be used to set the BPM offset in [um] only with BEST-ENC

### 1.4.5 1.8-15

- Added `getEnboxes()` `getEnboxesNumber()` functions

### 1.4.6 1.8-14

- Added `getTetramms` function

### 1.4.7 1.8-13

- Replaced `getBPMscaling_sel` with `getBPMscale` in `getPosArray_sel` and `getFFT`

### 1.4.8 1.8-12

- Compatibility with Best firmware  $\geq 1.2.12$
- Fixed set/get PidParamSingle

### 1.4.9 1.8-11

- Replaced get getBPMscaling with getBPMscale in getPosStats
- Added function getTetrammsNumber

### 1.4.10 1.8-10

- Removed setROC function
- Added scaling to set/getBPMoffset and set/getROClimits

### 1.4.11 1.8-9

- Added [getWindAvg\(\)](#) -> read PID update frequency from FPGA

### 1.4.12 1.8-8

- Added comments and documentation
- Added [setSetpoint2\(\)](#) -> set new setpoint reading scaling parameters from FPGA
- Added [getSetpoint2\(\)](#) -> get new setpoint reading scaling parameters from FPGA
- Added [getBPMoffset\(\)](#) -> read BPM offset from FPGA

### 1.4.13 1.8-7

- Added [getROClimits\(\)](#) -> read ROC limits from FPGA

### 1.4.14 1.8-6

- Added [getCrossBar\(\)](#) -> read Crossbar configuration from FPGA

### 1.4.15 1.8-5

- fixed [getBPMselector\(\)](#) -> read BPM selector configuration from FPGA

## 1.5 Funtion List

### 1.5.1 Data functions

List of all functions that access real-time data.

#### 1.5.1.1 `getBuffer`

---

#### 1.5.1.2 `getPosArray_sel`

#### 1.5.1.3 `getPosArray`

---

#### 1.5.1.4 `getVoltageArray`

#### 1.5.1.5 `getVoltageArray_sel`

---

#### 1.5.1.6 `getCurrentArray`

#### 1.5.1.7 `getCurrentArray_sel`

---

#### 1.5.1.8 `getFFT`

---

#### 1.5.1.9 `getPosStats`

#### 1.5.1.10 `setDisplayFreq`

#### 1.5.1.11 `getDisplayFreq`

---

#### 1.5.1.12 `getBPMStats`

---

### 1.5.2 Configuration functions

List of all configuration functions.

#### 1.5.2.1 `setOffsetSingle`

write single PID offset to FPGA (level 2)

#### 1.5.2.2 `getOffsetSingle`

read single PID offset from FPGA (no level)

#### 1.5.2.3 `setOffset`

write all 3 PID offsets to FPGA (level 2)

---

#### 1.5.2.4 `setWindAvg`

write average window samples of specific PID to FPGA (level 2)

#### 1.5.2.5 `getWindAvg` (added in v1.8-9)

read average window samples of specific PID from FPGA (no level)

---

#### 1.5.2.6 `setPIDparams`

write all the PID parameters of a specific PID to FPGA (level 2)

#### 1.5.2.7 `setPIDparamSingle`

write single PID parameter of a specific PID to FPGA (level 2)

#### 1.5.2.8 `getPIDparamSingle`

read single PID parameter of a specific PID from FPGA (no level)

---

**1.5.2.9 setPIDOutSel**

write PID output channel selector to FPGA (level 2) select which PID is assigned to which PreDAC output channel.

**1.5.2.10 getPIDOutSel**

read PID output channel selector from FPGA (no level)

---

**1.5.2.11 setCrossBar**

write TetrAMM crossbar configuration for BPM0 or BPM1 to FPGA (level 2)

**1.5.2.12 getCrossBar (1.8-6)**

read TetrAMM crossbar configuration for BPM0 or BPM1 from FPGA (no level)

---

**1.5.2.13 setBPMorient**

write BPM orientation for BPM0 or BPM1 to FPGA (level 2)

**1.5.2.14 getBPMorient**

read BPM orientation for BPM0 or BPM1 from FPGA (no level)

---

**1.5.2.15 setOutputMux**

write Output Mux configuration to FPGA (level 1, 2)

**1.5.2.16 getOutputMux**

read Output Mux configuration from FPGA (no level)

---

**1.5.2.17 setBPMscale**

write BPM0 or BPM1 scaling parameters to FPGA (level 2)

**1.5.2.18 getBPMscale**

read BPM0 or BPM1 scaling parameters from FPGA (no level)

**1.5.2.19 getBPMscaling\_sel**

read BPM0 or BPM1 scaling parameters from CONFIG\_FILE (no level)

**1.5.2.20 getBPMscaling**

read BPM0 scaling parameters from CONFIG\_FILE (no level)

---

**1.5.2.21 setBPMoffset**

write offsetX and offsetY of BPM0 or BPM1 to FPGA (level 2)

**1.5.2.22 getBPMoffset (1.8-8)**

read offsetX and offsetY of BPM0 or BPM1 from FPGA (no level)

---

**1.5.2.23 getBPMselector (1.8-5)**

read BPM selector configuration from FPGA (no level)

**1.5.2.24 setBPMselector**

write BPM selector configuration to FPGA (no level)!!!

---

**1.5.2.25 getROCandROI**

read RoiX, RoiY, RoiI0max, RoiI0min and Roc of BPM0 from CONFIG\_FILE (no level)

**1.5.2.26 getROCenable**

read ROC configuration of BPM0 and BPM1 from FPGA (no level)

**1.5.2.27 setROCenable**

write ROC configuration of BPM0 and BPM1 to FPGA (level 2)

**1.5.2.28 setROC**

write min and max ROC levels of specific axis to FPGA (level 2)

**1.5.2.29 setROClimits**

write min and max ROC levels of specific axis to FPGA (no level)

**1.5.2.30 getROClimits (1.8-7)**

read min and max ROC levels of specific axis from FPGA (no level)

**1.5.2.31 getROCandROI\_sel (TODO)**

read RoiX, RoiY, RoiI0max, RoiI0min and Roc of BPM0 or BPM1 from CONFIG\_FILE (no level)

---

**1.5.3 Access control functions**

List of all control functions.

**1.5.3.1 getLock**

Login function, inputs username and password, output login level

**1.5.3.2 getLockStatus**

returns the actual login level

---

**1.5.4 PID control functions****1.5.4.1 getPIDstatus**

read feedback status from FPGA (no level)

**1.5.4.2 setFBenable**

enable/disable feedback. Write to FPGA (level 1, 2)

**1.5.4.3 setCtrlReset**

reset feedback controller. Write to FPGA (level 1, 2)

**1.5.4.4 setPIDconf**

write PID configuration to FPGA (level 1, 2)

**1.5.4.5 getPIDconf**

read PID configuration from FPGA (no level)

---



#### 1.5.4.6 setSetpoint

reads RoiX, RoiY, RoiI0max, RoiI0min and Roc of BPM0 from CONFIG\_FILE, read BPM-selected scaling parameters (from CONFIG\_FILE), divides the user setpoint by the scaling parameters (from CONFIG\_FILE), sends the unscaled-setpoint to the FPGA (level 1, 2)

#### 1.5.4.7 getSetpoint

reads the setpoint from FPGA, reads RoiX, RoiY, RoiI0max, RoiI0min and Roc of BPM0 from CONFIG\_FILE, read BPM-selected scaling parameters (from CONFIG\_FILE), multiplied the setpoint by the scaling parameters returns the scaled-setpoints to user space

#### 1.5.4.8 setSetpoint2 (1.8-8)

read BPM selector configuration from FPGA, read BPM-selected scaling parameters (from FPGA), divides the user setpoint by the scaling parameters, sends the unscaled-setpoint to the FPGA (level 1, 2)

#### 1.5.4.9 getSetpoint2 (1.8-8)

reads the setpoint from FPGA, read BPM selector configuration from FPGA, read BPM-selected scaling parameters (from FPGA), multiplied the setpoint by the scaling parameters returns the scaled-setpoints to user space

---

#### 1.5.4.10 IIRcontrollerSetParam

write IIR coefficients to FPGA temporarily (level 2)

#### 1.5.4.11 IIRcontrollerCommitParams

apply IIR coefficients written to FPGA (level 2)

#### 1.5.4.12 IIRcontrollerGetParam

read IIR coefficients from FPGA (no level)

---

### 1.5.5 Function gen control functions

List of all functions to generate sinusoidal signal with PreDAC.

#### 1.5.5.1 funcGenConfig

#### 1.5.5.2 funcGenEnable

---

### 1.5.6 TetrAMM functions

List of all functions to communicate with TetrAMM.

#### 1.5.6.1 tetrammHVSetVoltage

#### 1.5.6.2 tetrammSetRange

#### 1.5.6.3 tetrammHVenable

---

### 1.5.7 EnBOX functions

List of all functions to communicate with EnBOX.

---

1.5.7.1 enboxEncoderSelect

1.5.7.2 enboxMathSelect

## 1.5.8 Misc functions

1.5.8.1 getSystemVersion

1.5.8.2 getSFPinfo

# Chapter 2

## Module Index

### 2.1 Modules

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# Chapter 3

## Class Index

### 3.1 Class List

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# Chapter 4

## File Index

### 4.1 File List

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# Chapter 5

## Module Documentation

### 5.1 Data functions

Functions which read measurement and calculation data from system.

#### Classes

- struct [best\\_buffer\\_line](#)
- struct [best\\_stats](#)

#### Functions

- struct `__attribute__((packed))` [best\\_buffer\\_line](#)
- int [getBuffer](#) (struct [best\\_buffer\\_line](#)[], int length)  
*Gets arrays of "samples" from display DMA.*
- int [getPosArray\\_sel](#) (int selector, double \*posX, double \*posY, double \*I0, unsigned int length)  
*Gets arrays of positions and intensity from display DMA.*
- int [getPosArray](#) (double \*posX, double \*posY, double \*I0, unsigned int length)  
*Same as [getPosArray\\_sel](#), but only for 1st TetrAMM.*
- int [getVoltageArray](#) (double(\*voltages)[4], unsigned int length)  
*Gets arrays of voltages from display DMA.*
- int [getVoltageArray\\_sel](#) (int sel, double(\*voltages)[4], unsigned int length)  
*Gets arrays of voltages one of X PreDAC (the PreDAC is only 1 for for the time beign)*
- int [getCurrentArray](#) (double(\*currents)[4], unsigned int length)  
*Gets arrays of currents from display DMA.*
- int [getCurrentArray\\_sel](#) (int sel, double(\*currents)[4], unsigned int length)  
*Gets arrays of currents one of two TetrAMMs.*
- int [getFFT](#) (unsigned int selector, double \*outM, double \*outF, int removedDC)  
*Calculates FFT.*
- int [getPosStats](#) (int selector, double \*meanX, double \*stdX, double \*meanY, double \*stdY, double \*meanI0, double \*stdI0)  
*Get position statistics.*
- int [setDisplayFreq](#) (int freq, uint8\_t use\_filter)  
*Sets display frequency.*
- int [getDisplayFreq](#) (int \*freq, uint8\_t \*use\_filter)  
*Gets display frequency.*
- struct `__attribute__((packed))` [best\\_stats](#)
- int [getBPMStats](#) ([best\\_stats](#) \*stats)  
*Gets BPM Statistics.*

### 5.1.1 Detailed Description

Functions which read measurement and calculation data from system.

### 5.1.2 Function Documentation

#### 5.1.2.1 `__attribute__()` [1/2]

```
struct __attribute__ (
    (__packed__)
```

#### 5.1.2.2 `__attribute__()` [2/2]

```
struct __attribute__ (
    (packed)
```

#### 5.1.2.3 `getBPMStats()`

```
int getBPMStats (
    best_stats * stats )
```

Gets BPM Statistics.

##### Parameters

<i>stats</i>	in <code>best_stats</code> structure format
--------------	---

##### Returns

0 on success, -1 on fail

#### 5.1.2.4 `getBuffer()`

```
int getBuffer (
    struct best_buffer_line [],
    int length )
```

Gets arrays of "samples" from display DMA.

##### Parameters

<i>length</i>	number of samples (each sample is 256 bytes big)
---------------	--

##### Returns

numbers of samples copies (should be equal to length)

#### 5.1.2.5 `getCurrentArray()`

```
int getCurrentArray (
    double(*) currents[4],
    unsigned int length )
```

Gets arrays of currents from display DMA.

**Parameters**

<i>length</i>	Length of the arrays
---------------	----------------------

**Returns**

numbers of samples copies (should be equal to length)

**5.1.2.6 getCurrentArray\_sel()**

```
int getCurrentArray_sel (
    int sel,
    double(*) currents[4],
    unsigned int length )
```

Gets arrays of currents one of two TetrAMMs.

**Parameters**

<i>sel</i>	TetrAMM selector (0 or 1)
<i>length</i>	Length of the arrays

**Returns**

numbers of samples copies (should be equal to length), or EINVAL if wrong arguments

**5.1.2.7 getDisplayFreq()**

```
int getDisplayFreq (
    int * freq,
    uint8_t * use_filter )
```

Gets display frequency.

**Returns**

0 on success, -1 on fail

**5.1.2.8 getFFT()**

```
int getFFT (
    unsigned int selector,
    double * outM,
    double * outF,
    int removeDC )
```

Calculates FFT.

**Parameters**

<i>selector</i>	Selects source for FFT ( offset_adc0_ch0 = 24, offset_pos0_posX = 0, offset_pos0_posY = 1, offset_pos0_I0 = 2, offset_pos1_posX = 3, offset_pos1_posY = 4, offset_pos1_I0 = 5, offset_dac0_ch0 = 16, offset_dac0_ch1 = 17, offset_dac0_ch2 = 18, offset_dac0_ch3 = 19
<i>outM</i>	magnitude output (size <code>FFT_LEN / 2</code> )
<i>outF</i>	frequency output (size <code>FFT_LEN / 2</code> )
<i>removeDC</i>	removes DC component

### 5.1.2.9 getPosArray()

```
int getPosArray (
    double * posX,
    double * posY,
    double * IO,
    unsigned int length )
```

Same as getPosArray\_sel, but only for 1st TetrAMM.

### 5.1.2.10 getPosArray\_sel()

```
int getPosArray_sel (
    int selector,
    double * posX,
    double * posY,
    double * IO,
    unsigned int length )
```

Gets arrays of positions and intensity from display DMA.

#### Parameters

<i>selector</i>	Selects BPM (= TetrAMM), should be 0 or 1
<i>posX</i>	Pointer to array where position X will be written
<i>posY</i>	Pointer to array where position Y will be written
<i>IO</i>	Pointer to array where Intensity will be written
<i>length</i>	Length of the arrays

#### Returns

numbers of samples copies (should be equal to length)

### 5.1.2.11 getPosStats()

```
int getPosStats (
    int selector,
    double * meanX,
    double * stdX,
    double * meanY,
    double * stdY,
    double * meanIO,
    double * stdIO )
```

Get position statistics.

#### Parameters

<i>selector</i>	Selects which TetrAmm
-----------------	-----------------------

### 5.1.2.12 getVoltageArray()

```
int getVoltageArray (
    double(*) voltages[4],
```

```
    unsigned int length )
```

Gets arrays of voltages from display DMA.

#### Parameters

<i>length</i>	Length of the arrays
---------------	----------------------

#### Returns

numbers of samples copies (should be equal to length)

#### 5.1.2.13 `getVoltageArray_sel()`

```
int getVoltageArray_sel (
    int sel,
    double(*) voltages[4],
    unsigned int length )
```

Gets arrays of voltages one of X PreDAC (the PreDAC is only 1 for for the time beign)

#### Parameters

<i>sel</i>	PreDAC selector (0, for the time beign)
<i>length</i>	Length of the arrays

#### Returns

numbers of samples copies (should be equal to length), or EINVAL if wrong arguments

#### 5.1.2.14 `setDisplayFreq()`

```
int setDisplayFreq (
    int freq,
    uint8_t use_filter )
```

Sets display frequency.

#### Parameters

<i>freq</i>	Frequency in hertz
<i>use_filter</i>	enable filter to prevent aliasing

#### Returns

0 on success, -1 on fail

#### Note

Access level must be user

## 5.2 Configuration functions

Functions which configure system.

### Enumerations

- enum `best_pid_select` { `BEST_PID_SELECT_X` = 0, `BEST_PID_SELECT_Y` = 1, `BEST_PID_SELECT_I0` = 2 }

### Functions

- int `getBPMscaling_sel` (int selector, double \*scaleX, double \*scaleY)  
*Gets BPM scaling parameters for position X and Y in [um]. Read values from CONFIG\_FILE.*
- int `getBPMscaling` (double \*scaleX, double \*scaleY)  
*Gets BPM scaling parameters of 1st BPM for position X and Y in [um]. Read values from CONFIG\_FILE.*
- int `getROCandROI` (double \*roiX, double \*roiY, double \*roi0min, double \*roi0max, double \*roc)  
*Gets RoiX, RoiY, Roi0max, Roi0min and Roc of 1st BPM. Read values from CONFIG\_FILE.*
- int `getROCEnable` (bool \*rocX, bool \*rocY, bool \*rocl0, bool \*rocX2, bool \*rocY2, bool \*rocl02)  
*Gets enables for different ROC checks of 1st nad 2nd BPMs. Read values from FPGA.*
- int `setROCEnable` (bool rocX, bool rocY, bool rocl0, bool rocX2, bool rocY2, bool rocl02)  
*Sets enables for different ROC checks of 1st nad 2nd BPMs. Writes values to FPGA.*
- int `setOffsetSingle` (enum `best_pid_select` pid\_sel, double offset)  
*Sets output offset in [V] (output=this+PID value, updated even when PID is not running). Write offset to FPGA.*
- int `getOffsetSingle` (enum `best_pid_select` pid\_sel, double \*offset)  
*Gets output offset in [V]. Read offset from FPGA.*
- int `setOffset` (double offsetX, double offsetY, double offsetI0)  
*Sets output offsets in [V] (output=this+PID value, updated even when PID is not running).*
- int `setWindAvg` (enum `best_pid_select` pid\_sel, uint32\_t nr\_samp)  
*Sets number of samples for window averaging.*
- int `getWindAvg` (enum `best_pid_select` pid\_sel, uint32\_t \*nr\_samp)  
*Gets number of samples for window averaging. Read number of samples from FPGA.*
- int `setPIDparams` (char pid\_sel, double Kp, double Ki, double Kd, double emin, double lmax, double Omin, double Omax, double Ogain)  
*Sets PID parameters (legacy).*
- int `setPIDparamSingle` (char pid\_sel, int pid\_par, double param\_value)  
*Sets single PID parameter of specific PID. Write PID parameter to FPGA.*
- int `getPIDparamSingle` (char pid\_sel, int pid\_par, double \*param\_value)  
*Gets PID parameters. Read PID parameters from FPGA.*
- int `setPIDOutSel` (uint8\_t posX, uint8\_t posY, uint8\_t I0)  
*Sets PID output cross switch. Selects which PID is assigned to which PreDAC output channel. Write PID output cross switch to FPGA.*
- int `getPIDOutSel` (uint8\_t \*posX, uint8\_t \*posY, uint8\_t \*I0)  
*Gets PID output cross switch. Read PID output cross switch from FPGA.*
- int `setCrossBar` (uint8\_t sel, uint8\_t ch0, uint8\_t ch1, uint8\_t ch2, uint8\_t ch3)  
*Sets input cross switch.*
- int `setBPMorient` (uint8\_t sel, uint8\_t is90deg)  
*Sets BPM orientation for diffOverSum.*
- int `setOutputMux` (uint8\_t sel)  
*Sets value of output mux.*
- int `getOutputMux` (uint8\_t \*sel)  
*Gets value of output mux.*
- int `setBPMscale` (double scaleX, double scaleY, int bpm)

- Sets BPM scaling parameters for position X and Y in [um]. Write BPM scaling parameters to FPGA.*

  - int `getBPMscale` (int selector, double \*scaleX, double \*scaleY)

*Gets BPM scaling parameters for position X and Y in [um]. Read BPM scaling parameters from FPGA.*
- int `setBPMoffset` (double offsetX, double offsetY, int bpm)

*Sets BPM offset in [um] ONLY FOR TETRAMMs. Write BPM offsets to FPGA.*
- int `setBPMoffsetEnbox` (double offsetX, double offsetY, int bpm, int encType)

*Sets BPM offset in [um] ONLY FOR Enboxes. Write BPM offsets to FPGA. (>= 1.8-16)*
- int `setBPMdevice` (uint8\_t sel, uint8\_t isEnbox)

*Sets BPM device for diffOverSum. Write to FPGA. (>= 1.8-18).*
- int `setEnboxType` (uint8\_t sel, uint8\_t encType)

*Sets the EnBOX type (relative or absolute) . Write to FPGA. (>= 1.8-18).*
- int `setEnboxSumDiff` (uint8\_t sel, uint8\_t isSumDiff)

*Set diffOverSum calculations (for EnBOX). 0:Normal the positions are calculated from enc1 and enc2. 1:SumDiff the positions are calculated from enc1+enc2 and enc1-enc2. Write to FPGA. (>= 1.8-18).*
- int `getBPMoffset` (double \*offsetX, double \*offsetY, int bpm)

*Gets BPM offsets in [um] ONLY FOR TETRAMMs. Read BPM offsets from FPGA.*
- int `getBPMoffsetEnbox` (double \*offsetX, double \*offsetY, int bpm, int encType)

*Gets BPM offsets in [um] ONLY FOR Enboxes. Read BPM offsets from FPGA.*
- int `getBPMorient` (uint8\_t sel, uint8\_t \*is90deg)

*Sets BPM orientation for diffOverSum. Read BPM orientation from FPGA.*
- int `getBPMselector` (int \*posX, int \*posY, int \*I0)

*Gets BPM sources. Read BPM sources from FPGA.*
- int `setBPMselector` (int posX, int posY, int I0)

*Sets BPM sources. Write BPM sources to FPGA.*
- int `getCrossBar` (uint8\_t sel, uint8\_t \*ch0, uint8\_t \*ch1, uint8\_t \*ch2, uint8\_t \*ch3)

*Gets input cross switch. Read input cross switch from FPGA.*
- int `setROClimits` (int sel, double min, double max)

*Sets ROC min and max values. ROC should be in [um] for positions and in [A] for intensity. Write min and max ROC levels of specific axis to FPGA.*
- int `getROClimits` (int sel, double \*min, double \*max)

*Gets ROC. ROC values are in [um] for positions and in [A] for intensity. Read min and max ROC levels of specific axis from FPGA.*

### 5.2.1 Detailed Description

Functions which configure system.

### 5.2.2 Enumeration Type Documentation

#### 5.2.2.1 best\_pid\_select

enum `best_pid_select`

Enumerator

BEST_PID_SELECT_X	
BEST_PID_SELECT_Y	
BEST_PID_SELECT_I0	

## 5.2.3 Function Documentation

### 5.2.3.1 getBPMoffset()

```
int getBPMoffset (
    double * offsetX,
    double * offsetY,
    int bpm )
```

Gets BPM offsets in [um] ONLY FOR TETRAMMs. Read BPM offsets from FPGA.

#### Parameters

<i>offsetX</i>	in [um]
<i>offsetY</i>	in [um]
<i>bpm</i>	Selects BPM (= TetrAMM), should be 0 or 1

#### Returns

0 on success, negative on fail

### 5.2.3.2 getBPMoffsetEnbox()

```
int getBPMoffsetEnbox (
    double * offsetX,
    double * offsetY,
    int bpm,
    int encType )
```

Gets BPM offsets in [um] ONLY FOR Enboxes. Read BPM offsets from FPGA.

#### Parameters

<i>offsetX</i>	in [um]
<i>offsetY</i>	in [um]
<i>bpm</i>	Selects BPM (= TetrAMM), should be 0 or 1
<i>encType,0</i>	Relative (tonic), 1: Absolute (Resolute)

#### Returns

0 on success, negative on fail

### 5.2.3.3 getBPMorient()

```
int getBPMorient (
    uint8_t sel,
    uint8_t * is90deg )
```

Sets BPM orientation for diffOverSum. Read BPM orientation from FPGA.

#### Parameters

<i>sel</i>	Selects TetrAmm (in case of 2 TetrAmms)
<i>is90deg</i>	(0 = 90deg, 1 = 45deg)



**Returns**

0 on success, negative on fail

**Note**

Access level: all levels

**5.2.3.4 getBPMscale()**

```
int getBPMscale (
    int selector,
    double * scaleX,
    double * scaleY )
```

Gets BPM scaling parameters for position X and Y in [um]. Read BPM scaling parameters from FPGA.

**Parameters**

<i>selector</i>	Selects BPM (= TetrAMM), should be 0 or 1
-----------------	---

**Returns**

0 on success, -1 on fail

**Note**

Access level: all levels

**5.2.3.5 getBPMscaling()**

```
int getBPMscaling (
    double * scaleX,
    double * scaleY )
```

Gets BPM scaling parameters of 1st BPM for position X and Y in [um]. Read values from CONFIG\_FILE.

**Parameters**

<i>scaleX</i>	scaling value for X in [um]
<i>scaleY</i>	scaling value for Y in [um]

**Returns**

0 on success, -1 on fail

**5.2.3.6 getBPMscaling\_sel()**

```
int getBPMscaling_sel (
    int selector,
    double * scaleX,
    double * scaleY )
```

Gets BPM scaling parameters for position X and Y in [um]. Read values from CONFIG\_FILE.

**Parameters**

<i>selector</i>	Selects BPM (= TetrAMM or EnBOX), 0 for 1st BPM, 1 for 2nd BPM
<i>scaleX</i>	pointer to scaling value for X in [um]
<i>scaleY</i>	pointer to scaling value for Y in [um]

**Returns**

0 on success, -1 on fail

**5.2.3.7 getBPMselector()**

```
int getBPMselector (
    int * posX,
    int * posY,
    int * IO )
```

Gets BPM sources. Read BPM sources from FPGA.

**Parameters**

<i>posX</i>	BPM (= TetrAMM) for posX
<i>posY</i>	BPM (= TetrAMM) for posY
<i>IO</i>	BPM (= TetrAMM) for IO

**Returns**

ACK

**Note**

Access level: all levels

**5.2.3.8 getCrossBar()**

```
int getCrossBar (
    uint8_t sel,
    uint8_t * ch0,
    uint8_t * ch1,
    uint8_t * ch2,
    uint8_t * ch3 )
```

Gets input cross switch. Read input cross switch from FPGA.

**Parameters**

<i>sel</i>	Selects TetrAmm (in case of 2 TetrAmms)
<i>ch0</i>	Top / Top-Left
<i>ch1</i>	Right / Top-Right
<i>ch2</i>	Bottom / Bottom-Right
<i>ch3</i>	Left / Bottom-Left

**Returns**

0 on success, negative on fail

**5.2.3.9 getOffsetSingle()**

```
int getOffsetSingle (
    enum best_pid_select pid_sel,
    double * offset )
```

Gets output offset in [V]. Read offset from FPGA.

**Parameters**

<i>pid_sel</i>	PID select enum
<i>offset</i>	offset value in [V]

**Returns**

0 on success, negative on fail

**Note**

Read from FPGA

Access level: all levels

**5.2.3.10 getOutputMux()**

```
int getOutputMux (
    uint8_t * sel )
```

Gets value of output mux.

**Parameters**

<i>sel</i>	Selects between FPGA (value 1) and software (value 0)
------------	---

**Returns**

0 on success, negative on fail

**Note**

Access level: all levels

**5.2.3.11 getPIDOutSel()**

```
int getPIDOutSel (
    uint8_t * posX,
    uint8_t * posY,
    uint8_t * IO )
```

Gets PID output cross switch. Read PID output cross switch from FPGA.

**Parameters**

<i>posX</i>	DAC channel for posX
-------------	----------------------

**Parameters**

<i>posY</i>	DAC channel for posY
<i>I0</i>	DAC channel for I0

**Returns**

0 on success, negative on fail

**Note**

Access level: all levels

**5.2.3.12 getPIDparamSingle()**

```
int getPIDparamSingle (
    char pid_sel,
    int pid_par,
    double * param_value )
```

Gets PID parameters. Read PID parameters from FPGA.

**Parameters**

<i>pid_sel</i>	(0 = X, 1 = Y, 2 = I0)
<i>pid_par</i>	(0=Kp, 1=Ki, 2=Kd, 3=emin, 4=lmax, 5=Omin, 6=Omax, 7=Ogain)
<i>param_value</i>	

**Returns**

0 on success, negative on fail

**Note**

Access level: all levels

**5.2.3.13 getROCandROI()**

```
int getROCandROI (
    double * roiX,
    double * roiY,
    double * roiI0min,
    double * roiI0max,
    double * roc )
```

Gets RoiX, RoiY, RoiI0max, RoiI0min and Roc of 1st BPM. Read values from CONFIG\_FILE.

**Parameters**

<i>roiX</i>	double parameter in [um]
<i>roiY</i>	double parameter in [um]
<i>roiI0min</i>	double parameter in [A]
<i>roiI0max</i>	double parameter in [A]
<i>roc</i>	double parameter in percentage

**Note**

Access level: all levels

**5.2.3.14 getROCEnable()**

```
int getROCEnable (
    bool * rocX,
    bool * rocY,
    bool * rocI0,
    bool * rocX2,
    bool * rocY2,
    bool * rocI02 )
```

Gets enables for different ROC checks of 1st nad 2nd BPMs. Read values from FPGA.

**Parameters**

<i>rocX</i>	enable rocX
<i>rocY</i>	enable rocY
<i>rocl0</i>	enable rocl0
<i>rocX2</i>	enable rocX2
<i>rocY2</i>	enable rocY2
<i>rocl02</i>	enable rocl02

**Returns**

0 on success, -1 on fail

**Note**

Access level: all levels

**5.2.3.15 getROClimits()**

```
int getROClimits (
    int sel,
    double * min,
    double * max )
```

Gets ROC. ROC values are in [um] for positions and in [A] for intensity. Read min and max ROC levels of specific axis from FPGA.

**Parameters**

<i>sel</i>	(0 = X, 1 = Y, 2 = I0, 0 = X2, 1 = Y2, 2 = I02)
<i>min</i>	
<i>max</i>	

**Returns**

0 on success, negative on fail

**5.2.3.16 getWindAvg()**

```
int getWindAvg (
```

```
enum best_pid_select pid_sel,
uint32_t * nr_samp )
```

Gets number of samples for window averaging. Read number of samples from FPGA.

#### Parameters

<i>sel</i>	(0 = X, 1 = Y, 2 = I0)
<i>nr_samp</i>	Number of samples

#### Returns

0 on success, negative on fail

### 5.2.3.17 setBPMdevice()

```
int setBPMdevice (
    uint8_t sel,
    uint8_t isEnbox )
```

Sets BPM device for diffOverSum. Write to FPGA. (>= 1.8-18).

#### Parameters

<i>sel</i>	Selects the BPM
<i>isEnbox</i>	

#### Returns

0 on success, negative on fail

#### Note

Access level must be admin

### 5.2.3.18 setBPMoffset()

```
int setBPMoffset (
    double offsetX,
    double offsetY,
    int bpm )
```

Sets BPM offset in [um] ONLY FOR TETRAMMs. Write BPM offsets to FPGA.

#### Parameters

<i>offsetX</i>	in [um]
<i>offsetY</i>	in [um]
<i>bpm</i>	Selects BPM (= TetrAMM), should be 0 or 1

#### Returns

0 on success, negative on fail

**Note**

Access level must be admin

**5.2.3.19 setBPMoffsetEnbox()**

```
int setBPMoffsetEnbox (
    double offsetX,
    double offsetY,
    int bpm,
    int encType )
```

Sets BPM offset in [um] ONLY FOR Enboxes. Write BPM offsets to FPGA. (>= 1.8-16)

**Parameters**

<i>offsetX</i>	in [um]
<i>offsetY</i>	in [um]
<i>bpm</i>	Selects BPM (= Enbox), should be 0 or 1
<i>encType,0</i>	Relative (tonic), 1: Absolute (Resolute)

**Returns**

0 on success, negative on fail

**Note**

Access level must be admin

**5.2.3.20 setBPMorient()**

```
int setBPMorient (
    uint8_t sel,
    uint8_t is90deg )
```

Sets BPM orientation for diffOverSum.

**Parameters**

<i>sel</i>	Selects TetrAmm (in case of 2 TetrAmms)
<i>is90deg</i>	

**Returns**

0 on success, negative on fail

**Note**

Access level must be admin

**5.2.3.21 setBPMscale()**

```
int setBPMscale (
    double scaleX,
    double scaleY,
    int bpm )
```

Sets BPM scaling parameters for position X and Y in [um]. Write BPM scaling parameters to FPGA.



## Parameters

<i>scaleX</i>	in [ $\mu$ m]
<i>scaleY</i>	in [ $\mu$ m]
<i>bpm</i>	Selects BPM (= TetrAMM), should be 0 or 1

## Returns

0 on success, -1 on fail

## Note

Access level must be admin

**5.2.3.22 setBPMselector()**

```
int setBPMselector (
    int posX,
    int posY,
    int IO )
```

Sets BPM sources. Write BPM sources to FPGA.

## Parameters

<i>posX</i>	BPM (= TetrAMM) for posX
<i>posY</i>	BPM (= TetrAMM) for posY
<i>IO</i>	BPM (= TetrAMM) for IO

## Returns

ACK

## Note

Access level: all levels

**5.2.3.23 setCrossBar()**

```
int setCrossBar (
    uint8_t sel,
    uint8_t ch0,
    uint8_t ch1,
    uint8_t ch2,
    uint8_t ch3 )
```

Sets input cross switch.

## Parameters

<i>sel</i>	Selects TetrAmm (in case of 2 TetrAmms)
<i>ch0</i>	
<i>ch1</i>	
<i>ch2</i>	
<i>ch3</i>	

**Returns**

0 on success, negative on fail

**Note**

Access level must be admin

**5.2.3.24 setEnboxSumDiff()**

```
int setEnboxSumDiff (
    uint8_t sel,
    uint8_t isSumDiff )
```

Set diffOverSum calculations (for EnBOX). 0:Normal the positions are calculated from enc1 and enc2. 1:SumDiff the positions are calculated from enc1+enc2 and enc1-enc2. Write to FPGA. (>= 1.8-18).

**Parameters**

<i>sel</i>	Selects the BPM
<i>encType,0</i>	Normal (enc1, enc2), 1: SumDiff (enc1+enc2, enc1-enc2)

**Returns**

0 on success, negative on fail

**Note**

Access level must be admin

**5.2.3.25 setEnboxType()**

```
int setEnboxType (
    uint8_t sel,
    uint8_t encType )
```

Sets the EnBOX type (relative or absolute) . Write to FPGA. (>= 1.8-18).

**Parameters**

<i>sel</i>	Selects the BPM
<i>encType,0</i>	Relative (tonic), 1: Absolute (Resolute)

**Returns**

0 on success, negative on fail

**Note**

Access level must be admin

**5.2.3.26 setOffset()**

```
int setOffset (
    double offsetX,
```

```
double offsetY,
double offsetIO )
```

Sets output offsets in [V] (output=this+PID value, updated even when PID is not running).

#### Parameters

<i>offsetX</i>	in [V]
<i>offsetY</i>	in [V]
<i>offsetIO</i>	in [V]

#### Returns

0 on success, negative on fail

#### Note

Access level must be admin

#### 5.2.3.27 setOffsetSingle()

```
int setOffsetSingle (
enum best_pid_select pid_sel,
double offset )
```

Sets output offset in [V] (output=this+PID value, updated even when PID is not running). Write offset to FPGA.

#### Parameters

<i>pid_sel</i>	PID select enum
<i>offset</i>	offset value in [V]

#### Returns

0 on success, negative on fail

#### Note

Write to FPGA

Access level must be admin

#### 5.2.3.28 setOutputMux()

```
int setOutputMux (
uint8_t sel )
```

Sets value of output mux.

#### Parameters

<i>sel</i>	Selects between FPGA (value 1) and software (value 0)
------------	---

**Returns**

0 on success, negative on fail

**Note**

Access level must be user or admin

**5.2.3.29 setPIDoutSel()**

```
int setPIDoutSel (
    uint8_t posX,
    uint8_t posY,
    uint8_t I0 )
```

Sets PID output cross switch. Selects which PID is assigned to which PreDAC output channel. Write PID output cross switch to FPGA.

**Parameters**

<i>posX</i>	DAC channel for posX
<i>posY</i>	DAC channel for posY
<i>I0</i>	DAC channel for I0

**Returns**

0 on success, negative on fail

**Note**

Access level must be admin

**5.2.3.30 setPIDparams()**

```
int setPIDparams (
    char pid_sel,
    double Kp,
    double Ki,
    double Kd,
    double emin,
    double Imax,
    double Omin,
    double Omax,
    double Ogain )
```

Sets PID parameters (legacy).

**Parameters**

<i>pid_sel</i>	(0 = X, 1 = Y, 2 = I0)
<i>Kp</i>	See BEST User's Manual for detailed explanation
<i>Ki</i>	See BEST User's Manual for detailed explanation
<i>Kd</i>	See BEST User's Manual for detailed explanation
<i>emin</i>	See BEST User's Manual for detailed explanation
<i>Imax</i>	See BEST User's Manual for detailed explanation
<i>Omin</i>	See BEST User's Manual for detailed explanation
<i>Omax</i>	See BEST User's Manual for detailed explanation
<i>Ogain</i>	See BEST User's Manual for detailed explanation

**Returns**

0 on success, negative on fail

**Note**

Access level must be admin

**5.2.3.31 setPIDparamSingle()**

```
int setPIDparamSingle (
    char pid_sel,
    int pid_par,
    double param_value )
```

Sets single PID parameter of specific PID. Write PID parameter to FPGA.

**Parameters**

<i>pid_sel</i>	(0 = X, 1 = Y, 2 = I0)
<i>pid_par</i>	(0=Kp, 1=Ki, 2=Kd, 3=emin, 4=Imax, 5=Omin, 6=Omax, 7=Ogain)
<i>param_value</i>	

**Returns**

0 on success, negative on fail

**Note**

Access level must be admin

**5.2.3.32 setROCEnable()**

```
int setROCEnable (
    bool rocX,
    bool rocY,
    bool rocI0,
    bool rocX2,
    bool rocY2,
    bool rocI02 )
```

Sets enables for different ROC checks of 1st nad 2nd BPMs. Writes values to FPGA.

**Parameters**

<i>rocX</i>	enable rocX
<i>rocY</i>	enable rocY
<i>rocI0</i>	enable rocI0
<i>rocX2</i>	enable rocX2
<i>rocY2</i>	enable rocY2
<i>rocI02</i>	enable rocI02

**Returns**

0 on success, -1 on fail

**Note**

Access level must be admin

**5.2.3.33 setROClimits()**

```
int setROClimits (
    int sel,
    double min,
    double max )
```

Sets ROC min and max values. ROC should be in [um] for positions and in [A] for intensity. Write min and max ROC levels of specific axis to FPGA.

**Parameters**

<i>sel</i>	(0 = X, 1 = Y, 2 = I0, 3 = X2, 4 = Y2, 5 = I02)
<i>min</i>	
<i>max</i>	

**Returns**

0 on success, negative on fail

**Note**

Access level: access level user or admin

**5.2.3.34 setWindAvg()**

```
int setWindAvg (
    enum best_pid_select pid_sel,
    uint32_t nr_samp )
```

Sets number of samples for window averaging.

**Parameters**

<i>sel</i>	(0 = X, 1 = Y, 2 = I0)
<i>nr_samp</i>	Number of samples

**Returns**

0 on success, negative on fail

**Note**

Access level must be admin

## 5.3 Access control functions

Functions which allow to elevate user access level.

### Functions

- int [getLock](#) (const char \*usr, const char \*pass)  
*Acquires lock on /var/lock/best\_lock.*
- int [getLockStatus](#) ()  
*Gets login level, which can be changed with call on [getLock\(\)](#).*

### 5.3.1 Detailed Description

Functions which allow to elevate user access level.

### 5.3.2 Function Documentation

#### 5.3.2.1 getLock()

```
int getLock (
    const char * usr,
    const char * pass )
```

Acquires lock on /var/lock/best\_lock.

#### Parameters

<i>usr</i>	Username
<i>pass</i>	Password

#### Returns

login level (0=cruise, 1=user, 2=admin)

#### Note

Access level: all levels

#### 5.3.2.2 getLockStatus()

```
int getLockStatus ( )
```

Gets login level, which can be changed with call on [getLock\(\)](#).

#### Returns

login level (0=cruise, 1=user, 2=admin)

#### Note

Access level: all levels

## 5.4 PID control functions

Functions which control PID behaviour.

### Functions

- unsigned int [getPIDstatus](#) (void)  
*Gets PID status.*
- int [setFBenable](#) (int enable)  
*Enables or disables feedback.*
- int [setCtrlReset](#) ()  
*Resets internal states of controller.*
- int [setPIDconf](#) (char conf)  
*Sets PID controller configuration. Write PID controller configuration to FPGA.*
- int [getPIDconf](#) (char \*conf)  
*Sets PID controller configuration. Read PID configuration from FPGA.*
- int [setSetpoint](#) (char sel, double setpt)  
*Sets new setpoint in [um]. Write setpoint to FPGA. Before writing to FPGA the setpoint is divided by scaling parameter of 1st BPM. Scaling parameters are read from CONFIG\_FILE.*
- int [getSetpoint](#) (char sel, double \*setpt)  
*Gets setpoint in [um]. Read setpoint from FPGA. After reading from FPGA the setpoint is multiplied by scaling parameter. Scaling parameters are read from CONFIG\_FILE.*
- int [setSetpoint2](#) (char sel, double setpt)  
*Sets new setpoint (without using config file). Write setpoint to FPGA. Before writing to FPGA the setpoint is divided by scaling parameter. Scaling parameters are read from FPGA.*
- int [getSetpoint2](#) (char sel, double \*setpt)  
*Gets new setpoint (without using config file). Read setpoint from FPGA. After reading from FPGA the setpoint is multiplied by scaling parameter. Scaling parameters are read from FPGA.*
- int [setSetpointEnbox](#) (char sel, double setpt)  
*Sets new setpoint (without using config file) for Enbox ONLY. Write setpoint to FPGA. Before writing to FPGA the setpoint is NOT divided by scaling parameter. Scaling parameters are read from FPGA.*
- int [getSetpointEnbox](#) (char sel, double \*setpt)  
*Gets new setpoint (without using config file) for Enbox ONLY. Read setpoint from FPGA. After reading from FPGA the setpoint is NOT multiplied by scaling parameter. Scaling parameters are read from FPGA.*
- int [IIRcontrollerSetParam](#) (char ctrlr\_sel, char param\_is\_a, char param\_sel, double param)  
*Sets IIR controller parameter.*
- int [IIRcontrollerCommitParams](#) (char ctrlr\_sel)  
*Commits IIR controller parameters.*
- int [IIRcontrollerGetParam](#) (char ctrlr\_sel, char param\_is\_a, char param\_sel, double \*param)  
*Reads IIR controller parameter.*

### 5.4.1 Detailed Description

Functions which control PID behaviour.

### 5.4.2 Function Documentation

#### 5.4.2.1 getPIDconf()

```
int getPIDconf (
    char * conf )
```

Sets PID controller configuration. Read PID configuration from FPGA.



## Parameters

<i>conf</i>	return PID conf
-------------	-----------------

## Returns

0 on success, negative on fail

5.4.2.2 `getPIDstatus()`

```
unsigned int getPIDstatus (
    void )
```

Gets PID status.

## Returns

stoppedByUser = 0, stoppedByROC = 1, paused = 2, running = 3

## Note

Access level: all levels

5.4.2.3 `getSetpoint()`

```
int getSetpoint (
    char sel,
    double * setpt )
```

Gets setpoint in [um]. Read setpoint from FPGA. After reading from FPGA the setpoint is multiplied by scaling parameter. Scaling parameters are read from CONFIG\_FILE.

## Parameters

<i>sel</i>	Selects which PID ( X = 0, Y = 1, I0 = 2 )
<i>setpt</i>	Setpoint in [um]

## Returns

0 on success, negative on fail

## Note

Access level: all levels

5.4.2.4 `getSetpoint2()`

```
int getSetpoint2 (
    char sel,
    double * setpt )
```

Gets new setpoint (without using config file). Read setpoint from FPGA. After reading from FPGA the setpoint is multiplied by scaling parameter. Scaling parameters are read from FPGA.

## Parameters

<i>sel</i>	Selects which PID ( X = 0, Y = 1, I0 = 2 )
<i>setpt</i>	Setpoint in [um]

**Returns**

0 on success, negative on fail

**5.4.2.5 getSetpointEnbox()**

```
int getSetpointEnbox (
    char sel,
    double * setpt )
```

Gets new setpoint (without using config file) for Enbox ONLY. Read setpoint from FPGA. After reading from FPGA the setpoint is NOT multiplied by scaling parameter. Scaling parameters are read from FPGA.

**Parameters**

<i>sel</i>	Selects which PID ( X = 0, Y = 1, I0 = 2 )
<i>setpt</i>	Setpoint in [um]

**Returns**

0 on success, negative on fail

**5.4.2.6 IIRcontrollerCommitParams()**

```
int IIRcontrollerCommitParams (
    char ctrlr_sel )
```

Commits IIR controller parameters.

**Parameters**

<i>ctrlr_sel</i>	Selects controller ( X = 0, Y = 1, I0 = 2 )
------------------	---

**Returns**

0 on success, negative on fail

**5.4.2.7 IIRcontrollerGetParam()**

```
int IIRcontrollerGetParam (
    char ctrlr_sel,
    char param_is_a,
    char param_sel,
    double * param )
```

Reads IIR controller parameter.

**Parameters**

<i>ctrlr_sel</i>	Selects controller ( X = 0, Y = 1, I0 = 2 )
<i>param_is_a</i>	Selects if param read is a (when 1) or b (when 0)
<i>param_sel</i>	Selects parameter (0-9)
<i>param</i>	IIR coefficient

**Returns**

0 on success, negative on fail

This function returns the parameter from read-back storage (the parameters which are actually being used by IIR controller).

**5.4.2.8 IIRcontrollerSetParam()**

```
int IIRcontrollerSetParam (
    char ctrlr_sel,
    char param_is_a,
    char param_sel,
    double param )
```

Sets IIR controller parameter.

**Parameters**

<i>ctrlr_sel</i>	Selects controller ( X = 0, Y = 1, I0 = 2 )
<i>param_is_a</i>	Selects if param written is a (when 1) or b (when 0)
<i>param_sel</i>	Selects parameter (0-9)
<i>param</i>	IIR coefficient

**Returns**

0 on success, negative on fail

This function stores the parameter in the temporary storage. After all parameters have been set use IIRcontrollerCommitParams function to download parameters to controller.

**5.4.2.9 setCtrlReset()**

```
int setCtrlReset ( )
```

Resets internal states of controller.

**Returns**

0 on success, negative on fail (e.g. authentication)

**Note**

Access level must be user or admin

**5.4.2.10 setFBenable()**

```
int setFBenable (
    int enable )
```

Enables or disables feedback.

**Returns**

0 on success, negative on fail (e.g. authentication)

**Note**

Access level must be user or admin

#### 5.4.2.11 setPIDconf()

```
int setPIDconf (
    char conf )
```

Sets PID controller configuration. Write PID controller configuration to FPGA.

##### Parameters

<i>conf</i>	PID conf (X = 0, XY = 1, XIO = 2, Y = 3, YIO = 4, XYIO = 5, IO = 6)
-------------	---

##### Returns

0 on success, negative on fail (e.g. authentication)

##### Note

Access level must be user or admin

#### 5.4.2.12 setSetpoint()

```
int setSetpoint (
    char sel,
    double setpt )
```

Sets new setpoint in [um]. Write setpoint to FPGA. Before writing to FPGA the setpoint is divided by scaling parameter of 1st BPM. Scaling parameters are read from CONFIG\_FILE.

##### Parameters

<i>sel</i>	Selects which PID ( X = 0, Y = 1, IO = 2 )
<i>setpt</i>	Setpoint in [um]

##### Returns

0 on success, negative on fail (e.g. authentication)

##### Note

Access level must be user or admin

#### 5.4.2.13 setSetpoint2()

```
int setSetpoint2 (
    char sel,
    double setpt )
```

Sets new setpoint (without using config file). Write setpoint to FPGA. Before writing to FPGA the setpoint is divided by scaling parameter. Scaling parameters are read from FPGA.

##### Parameters

<i>sel</i>	Selects which PID ( X = 0, Y = 1, IO = 2 )
<i>setpt</i>	Setpoint in [um]

**Returns**

0 on success, negative on fail (e.g. authentication)

**Note**

Access level must be user or admin

**5.4.2.14 setSetpointEnbox()**

```
int setSetpointEnbox (  
    char sel,  
    double setpt )
```

Sets new setpoint (without using config file) for Enbox ONLY. Write setpoint to FPGA. Before writing to FPGA the setpoint is NOT divided by scaling parameter. Scaling parameters are read from FPGA.

**Parameters**

<i>sel</i>	Selects which PID ( X = 0, Y = 1, I0 = 2 )
<i>setpt</i>	Setpoint in [um]

**Returns**

0 on success, negative on fail (e.g. authentication)

**Note**

Access level must be user or admin

## 5.5 Function generator functions

### Functions

- int `funcGenConfig` (float `freq`, float `ampl[4]`, float `offset[4]`)  
*Configures function generator.*
- int `funcGenEnable` (char `enable`)  
*Enables or disables function generator.*

### 5.5.1 Detailed Description

### 5.5.2 Function Documentation

#### 5.5.2.1 `funcGenConfig()`

```
int funcGenConfig (
    float freq,
    float ampl[4],
    float offset[4] )
```

Configures function generator.

#### Parameters

<i>freq</i>	Frequency (in Hz)
<i>ampl</i>	Amplitude of sine wave
<i>offset</i>	Offset of sine wave

#### Returns

0 on success, negative on fail

configuration is committed when the func gen is enabled. if func gen is already running, just call `funcGenEnable` function again

#### 5.5.2.2 `funcGenEnable()`

```
int funcGenEnable (
    char enable )
```

Enables or disables function generator.

#### Parameters

<i>enable</i>	0 to disable, 1 to enable
---------------	---------------------------

## 5.6 TetrAMM functions

### Functions

- int `tetrammSetRange` (int range, unsigned char selector)  
*Sets front-end range.*
- int `tetrammHVSetVoltage` (float voltage, unsigned char sel)  
*Sets TetrAMM HV voltage.*
- int `tetrammHVenable` (int enable, unsigned char sel)  
*Enables or disables TetrAMM HV module.*
- unsigned int `getTetrammsNumber` ()  
*Gets number of TetrAMMs.*
- int `getTetramms` (int \*pos\_A, int \*pos\_B)  
*Gets TetrAMMs. (>= 1.8-14).*

### 5.6.1 Detailed Description

### 5.6.2 Function Documentation

#### 5.6.2.1 `getTetramms()`

```
int getTetramms (
    int * pos_A,
    int * pos_B )
```

Gets TetrAMMs. (>= 1.8-14).

#### Parameters

<code>pos↔ _A</code>	on SFP A, 0 = No TetrAMM connected, 1 = TetrAMM connected
<code>pos↔ _A</code>	on SFP B, 0 = No TetrAMM connected, 1 = TetrAMM connected

#### Returns

0 on success, negative on fail

#### 5.6.2.2 `getTetrammsNumber()`

```
unsigned int getTetrammsNumber ( )
```

Gets number of TetrAMMs.

#### Returns

TetrAMMs number

#### 5.6.2.3 `tetrammHVenable()`

```
int tetrammHVenable (
    int enable,
    unsigned char sel )
```

Enables or disables TetrAMM HV module.

**Parameters**

<i>enable</i>	1 to enable, 0 to disable
<i>sel</i>	0 = TetrAMM on SFP A, 1 = TetrAMM on SFP B

**Note**

Access level must be admin

**5.6.2.4 tetrammHVSetVoltage()**

```
int tetrammHVSetVoltage (
    float voltage,
    unsigned char sel )
```

Sets TetrAMM HV voltage.

**Parameters**

<i>voltage</i>	voltage in volts
<i>sel</i>	0 = TetrAMM on SFP A, 1 = TetrAMM on SFP B

**Note**

Access level must be admin

**5.6.2.5 tetrammSetRange()**

```
int tetrammSetRange (
    int range,
    unsigned char selector )
```

Sets front-end range.

**Parameters**

<i>range</i>	0 = RNG0, 1 = RNG1
<i>selector</i>	0 = TetrAMM on SFP A, 1 = TetrAMM on SFP B

**Note**

Access level must be admin



## 5.7 EnBOX functions

### Functions

- int `enboxEncoderSelect` (int `enc_type`, unsigned char `selector`)
- unsigned int `getEnboxesNumber` ()
  - Gets number of Enbox. (>= 1.8-15).*
- int `getEnboxes` (int `*pos_A`, int `*pos_B`)
  - Gets Enbox. (>= 1.8-15).*

### 5.7.1 Detailed Description

### 5.7.2 Function Documentation

#### 5.7.2.1 `enboxEncoderSelect()`

```
int enboxEncoderSelect (
    int enc_type,
    unsigned char selector )
```

##### Parameters

<i>enc_type</i>	0 = Tonic, 1 = Absolute
<i>selector</i>	0 = EnBOX on SFP A, 1 = EnBOX on SFP B

##### Note

Access level must be admin

#### 5.7.2.2 `getEnboxes()`

```
int getEnboxes (
    int * pos_A,
    int * pos_B )
```

Gets Enbox. (>= 1.8-15).

##### Parameters

<i>pos↔ _A</i>	on SFP A, 0 = No Enbox connected, 1 = Enbox connected
<i>pos↔ _A</i>	on SFP B, 0 = No Enbox connected, 1 = Enbox connected

##### Returns

0 on success, negative on fail

#### 5.7.2.3 `getEnboxesNumber()`

```
unsigned int getEnboxesNumber ( )
```

Gets number of Enbox. (>= 1.8-15).

**Returns**

Enboxes number

## 5.8 Misc

### Functions

- int [getSFPinfo](#) (int selector, uint16\_t \*id, uint32\_t \*timestamp, uint32\_t \*fw\_ver, uint32\_t \*ser\_nr)  
*Gets SFP info.*
- int [getSystemVersion](#) (uint32\_t \*hwVer, uint32\_t \*swVer, uint32\_t \*ctrlVer)  
*Gets System HW, SW and PID versions.*

#### 5.8.1 Detailed Description

#### 5.8.2 Function Documentation

##### 5.8.2.1 getSFPinfo()

```
int getSFPinfo (
    int selector,
    uint16_t * id,
    uint32_t * timestamp,
    uint32_t * fw_ver,
    uint32_t * ser_nr )
```

Gets SFP info.

##### Parameters

<i>selector</i>	Selects which SFP port (A=0, B=1, ...)
<i>id</i>	
<i>timestamp</i>	
<i>fw_ver</i>	
<i>ser_nr</i>	

##### Returns

0 on success, -1 on fail

##### Note

Access level: all levels

##### 5.8.2.2 getSystemVersion()

```
int getSystemVersion (
    uint32_t * hwVer,
    uint32_t * swVer,
    uint32_t * ctrlVer )
```

Gets System HW, SW and PID versions.

##### Parameters

<i>hwVer</i>	hardware version
<i>swVer</i>	software version (main application)
<i>ctrlVer</i>	controller version (pid version)

**Returns**

0 on success, -1 on fail

**Note**

Access level: all levels

## Chapter 6

# Class Documentation

### 6.1 `best_buffer_line` Struct Reference

```
#include <best_c_interface.h>
```

#### 6.1.1 Detailed Description

best DMA buffer struct

The documentation for this struct was generated from the following file:

- [best\\_c\\_interface.h](#)

### 6.2 `best_stats` Struct Reference

```
#include <best_c_interface.h>
```

#### 6.2.1 Detailed Description

best statistics data structure

The documentation for this struct was generated from the following file:

- [best\\_c\\_interface.h](#)



# Chapter 7

## File Documentation

### 7.1 best\_c\_interface.c File Reference

```
#include "best_c_interface.h"
#include <stdlib.h>
#include <time.h>
#include <stdint.h>
#include <string.h>
#include <errno.h>
#include <unistd.h>
#include <fcntl.h>
#include <sys/stat.h>
#include <sys/ioctl.h>
#include "pcie_driver/BEST_PCIE.h"
#include "pcie_mailbox/mailbox_comm_defs.h"
#include <iostream>
#include "inih/cpp/INIReader.h"
#include <complex>
#include <fftw3.h>
#include <signal.h>
```

#### Macros

- `#define VERSION_STRING_HELPER(X) #X`
- `#define VERSION_STRING(X) VERSION_STRING_HELPER(X)`
- `#define LIB_BEST_DEBUG 0`
- `#define debug_print(fmt, ...) do { if (LIB_BEST_DEBUG) fprintf(stderr, "[libbest] " fmt, __VA_ARGS__); } while (0)`

#### Functions

- `int getBestLock (int fd)`  
*!!! All sets should check loginLevel*
- `int releaseBestLock (int fd)`
- `void lock_sigaction (int sig, siginfo_t *siginfo, void *context)`
- `void __attribute__((constructor))`
- `void __attribute__((destructor))`
- `int getLock (const char *usr, const char *pass)`  
*Acquires lock on /var/lock/best\_lock.*
- `int getLockStatus ()`  
*Gets login level, which can be changed with call on getLock().*
- `int setFBenable (int enable)`

- Enables or disables feedback.*

  - int [setCtrlReset](#) ()

*Resets internal states of controller.*
- int [setPIDconf](#) (char conf)
- Sets PID controller configuration. Write PID controller configuration to FPGA.*
- int [getPIDconf](#) (char \*conf)
- Sets PID controller configuration. Read PID configuration from FPGA.*
- int [setSetpoint](#) (char sel, double setpt)
- Sets new setpoint in [um]. Write setpoint to FPGA. Before writing to FPGA the setpoint is divided by scaling parameter of 1st BPM. Scaling parameters are read from CONFIG\_FILE.*
- int [setSetpoint2](#) (char sel, double setpt)
- Sets new setpoint (without using config file). Write setpoint to FPGA. Before writing to FPGA the setpoint is divided by scaling parameter. Scaling parameters are read from FPGA.*
- int [setSetpointEnbox](#) (char sel, double setpt)
- Sets new setpoint (without using config file) for Enbox ONLY. Write setpoint to FPGA. Before writing to FPGA the setpoint is NOT divided by scaling parameter. Scaling parameters are read from FPGA.*
- int [getSetpoint](#) (char sel, double \*setpt)
- Gets setpoint in [um]. Read setpoint from FPGA. After reading from FPGA the setpoint is multiplied by scaling parameter. Scaling parameters are read from CONFIG\_FILE.*
- int [getSetpoint2](#) (char sel, double \*setpt)
- Gets new setpoint (without using config file). Read setpoint from FPGA. After reading from FPGA the setpoint is multiplied by scaling parameter. Scaling parameters are read from FPGA.*
- int [getSetpointEnbox](#) (char sel, double \*setpt)
- Gets new setpoint (without using config file) for Enbox ONLY. Read setpoint from FPGA. After reading from FPGA the setpoint is NOT multiplied by scaling parameter. Scaling parameters are read from FPGA.*
- int [IIRcontrollerSetParam](#) (char ctrlr\_sel, char param\_is\_a, char param\_sel, double param)
- Sets IIR controller parameter.*
- int [IIRcontrollerCommitParams](#) (char ctrlr\_sel)
- Commits IIR controller parameters.*
- int [IIRcontrollerGetParam](#) (char ctrlr\_sel, char param\_is\_a, char param\_sel, double \*param)
- Reads IIR controller parameter.*
- int [funcGenConfig](#) (float freq, float ampl[4], float offset[4])
- Configures function generator.*
- int [funcGenEnable](#) (char enable)
- Enables or disables function generator.*
- int [getSFPinfo](#) (int selector, uint16\_t \*id, uint32\_t \*timestamp, uint32\_t \*fw\_ver, uint32\_t \*ser\_nr)
- Gets SFP info.*
- int [getSystemVersion](#) (uint32\_t \*hwVer, uint32\_t \*swVer, uint32\_t \*ctrlVer)
- Gets System HW, SW and PID versions.*

## Variables

- double \* [in](#)
- fftw\_complex \* [out](#)
- fftw\_plan [p](#)
- int [fd\\_DMA](#)
- int [fd\\_Mbox](#)
- int [fd\\_Lock](#)
- int [SAMP\\_FREQ](#) = 1000
- int [loginLevel](#) = 0
- char [LIB\\_BEST\\_VERSION](#) [] = [VERSION\\_STRING](#)(FROM\_DEFS\_VERSION)



## 7.1.1 Macro Definition Documentation

### 7.1.1.1 debug\_print

```
#define debug_print(  
    fmt,  
    ... ) do { if (LIB_BEST_DEBUG) fprintf(stderr, "[libbest] " fmt, __VA_ARGS__);  
} while (0)
```

### 7.1.1.2 LIB\_BEST\_DEBUG

```
#define LIB_BEST_DEBUG 0
```

### 7.1.1.3 VERSION\_STRING

```
#define VERSION_STRING(  
    X ) VERSION_STRING_HELPER(X)
```

### 7.1.1.4 VERSION\_STRING\_HELPER

```
#define VERSION_STRING_HELPER(  
    X ) #X
```

## 7.1.2 Function Documentation

### 7.1.2.1 \_\_attribute\_\_() [1/2]

```
void __attribute__ (  
    (constructor) )
```

### 7.1.2.2 \_\_attribute\_\_() [2/2]

```
void __attribute__ (  
    (destructor) )
```

### 7.1.2.3 getBestLock()

```
int getBestLock (  
    int fd )
```

!!! All sets should check loginLevel

### 7.1.2.4 lock\_sigaction()

```
void lock_sigaction (  
    int sig,  
    siginfo_t * siginfo,  
    void * context )
```

### 7.1.2.5 releaseBestLock()

```
int releaseBestLock (  
    int fd )
```

## 7.1.3 Variable Documentation

### 7.1.3.1 fd\_DMA

```
int fd_DMA
```

### 7.1.3.2 fd\_Lock

```
int fd_Lock
```

### 7.1.3.3 fd\_Mbox

```
int fd_Mbox
```

### 7.1.3.4 in

```
double* in
```

### 7.1.3.5 LIB\_BEST\_VERSION

```
char LIB_BEST_VERSION[] = VERSION_STRING (FROM_DEFS_VERSION)
```

### 7.1.3.6 loginLevel

```
int loginLevel = 0
```

### 7.1.3.7 out

```
fftw_complex* out
```

### 7.1.3.8 p

```
fftw_plan p
```

### 7.1.3.9 SAMP\_FREQ

```
int SAMP_FREQ = 1000
```

## 7.2 best\_c\_interface.h File Reference

```
#include <stdint.h>
```

## Macros

- #define `BEST_FILE_MAILBOX` `"/dev/best_mailbox"`
- #define `BEST_FILE_DISP_DMA` `"/dev/best_dma_displ"`
- #define `LOCK_FILE` `"/var/lock/best_lock"`
- #define `BEST_FILE_CONFIG_INI` `"/opt/CAENels/BEST/config.ini"`
- #define `FFT_LEN` (2048)
- #define `LEN_BBF` (32)

## Enumerations

- enum `best_pid_select` { `BEST_PID_SELECT_X` = 0, `BEST_PID_SELECT_Y` = 1, `BEST_PID_SELECT_I0` = 2 }

## Functions

- struct `__attribute__((packed)) best_buffer_line`
- int `getBuffer` (struct `best_buffer_line`[], int length)  
*Gets arrays of "samples" from display DMA.*
- int `getPosArray_sel` (int selector, double \*posX, double \*posY, double \*I0, unsigned int length)  
*Gets arrays of positions and intensity from display DMA.*
- int `getPosArray` (double \*posX, double \*posY, double \*I0, unsigned int length)  
*Same as getPosArray\_sel, but only for 1st TetrAMM.*
- int `getVoltageArray` (double(\*voltages)[4], unsigned int length)  
*Gets arrays of voltages from display DMA.*
- int `getVoltageArray_sel` (int sel, double(\*voltages)[4], unsigned int length)  
*Gets arrays of voltages one of X PreDAC (the PreDAC is only 1 for the time beign)*
- int `getCurrentArray` (double(\*currents)[4], unsigned int length)  
*Gets arrays of currents from display DMA.*
- int `getCurrentArray_sel` (int sel, double(\*currents)[4], unsigned int length)  
*Gets arrays of currents one of two TetrAMMs.*
- int `getFFT` (unsigned int selector, double \*outM, double \*outF, int removedDC)  
*Calculates FFT.*
- int `getPosStats` (int selector, double \*meanX, double \*stdX, double \*meanY, double \*stdY, double \*meanI0, double \*stdI0)  
*Get position statistics.*
- int `setDisplayFreq` (int freq, uint8\_t use\_filter)  
*Sets display frequency.*
- int `getDisplayFreq` (int \*freq, uint8\_t \*use\_filter)  
*Gets display frequency.*
- struct `__attribute__((packed)) best_stats`
- int `getBPMStats` (`best_stats` \*stats)  
*Gets BPM Statistics.*
- int `getBPMscaling_sel` (int selector, double \*scaleX, double \*scaleY)  
*Gets BPM scaling parameters for position X and Y in [um]. Read values from CONFIG\_FILE.*
- int `getBPMscaling` (double \*scaleX, double \*scaleY)  
*Gets BPM scaling parameters of 1st BPM for position X and Y in [um]. Read values from CONFIG\_FILE.*
- int `getROCandROI` (double \*roiX, double \*roiY, double \*roiI0min, double \*roiI0max, double \*roc)  
*Gets RoiX, RoiY, RoiI0max, RoiI0min and Roc of 1st BPM. Read values from CONFIG\_FILE.*
- int `getROCEnable` (bool \*rocX, bool \*rocY, bool \*rocl0, bool \*rocX2, bool \*rocY2, bool \*rocl02)  
*Gets enables for different ROC checks of 1st nad 2nd BPMs. Read values from FPGA.*
- int `setROCEnable` (bool rocX, bool rocY, bool rocl0, bool rocX2, bool rocY2, bool rocl02)  
*Sets enables for different ROC checks of 1st nad 2nd BPMs. Writes values to FPGA.*

- int [setOffsetSingle](#) (enum [best\\_pid\\_select](#) pid\_sel, double offset)  
*Sets output offset in [V] (output=this+PID value, updated even when PID is not running). Write offset to FPGA.*
- int [getOffsetSingle](#) (enum [best\\_pid\\_select](#) pid\_sel, double \*offset)  
*Gets output offset in [V]. Read offset from FPGA.*
- int [setOffset](#) (double offsetX, double offsetY, double offsetI0)  
*Sets output offsets in [V] (output=this+PID value, updated even when PID is not running).*
- int [setWindAvg](#) (enum [best\\_pid\\_select](#) pid\_sel, uint32\_t nr\_samp)  
*Sets number of samples for window averaging.*
- int [getWindAvg](#) (enum [best\\_pid\\_select](#) pid\_sel, uint32\_t \*nr\_samp)  
*Gets number of samples for window averaging. Read number of samples from FPGA.*
- int [setPIDparams](#) (char pid\_sel, double Kp, double Ki, double Kd, double emin, double lmax, double Omin, double Omax, double Ogain)  
*Sets PID parameters (legacy).*
- int [setPIDparamSingle](#) (char pid\_sel, int pid\_par, double param\_value)  
*Sets single PID parameter of specific PID. Write PID parameter to FPGA.*
- int [getPIDparamSingle](#) (char pid\_sel, int pid\_par, double \*param\_value)  
*Gets PID parameters. Read PID parameters from FPGA.*
- int [setPIDOutSel](#) (uint8\_t posX, uint8\_t posY, uint8\_t I0)  
*Sets PID output cross switch. Selects which PID is assigned to which PreDAC output channel. Write PID output cross switch to FPGA.*
- int [getPIDOutSel](#) (uint8\_t \*posX, uint8\_t \*posY, uint8\_t \*I0)  
*Gets PID output cross switch. Read PID output cross switch from FPGA.*
- int [setCrossBar](#) (uint8\_t sel, uint8\_t ch0, uint8\_t ch1, uint8\_t ch2, uint8\_t ch3)  
*Sets input cross switch.*
- int [setBPMorient](#) (uint8\_t sel, uint8\_t is90deg)  
*Sets BPM orientation for diffOverSum.*
- int [setOutputMux](#) (uint8\_t sel)  
*Sets value of output mux.*
- int [getOutputMux](#) (uint8\_t \*sel)  
*Gets value of output mux.*
- int [setBPMscale](#) (double scaleX, double scaleY, int bpm)  
*Sets BPM scaling parameters for position X and Y in [um]. Write BPM scaling parameters to FPGA.*
- int [getBPMscale](#) (int selector, double \*scaleX, double \*scaleY)  
*Gets BPM scaling parameters for position X and Y in [um]. Read BPM scaling parameters from FPGA.*
- int [setBPMoffset](#) (double offsetX, double offsetY, int bpm)  
*Sets BPM offset in [um] ONLY FOR TETRAMMs. Write BPM offsets to FPGA.*
- int [setBPMoffsetEnbox](#) (double offsetX, double offsetY, int bpm, int encType)  
*Sets BPM offset in [um] ONLY FOR Enboxes. Write BPM offsets to FPGA. (>= 1.8-16)*
- int [setBPMdevice](#) (uint8\_t sel, uint8\_t isEnbox)  
*Sets BPM device for diffOverSum. Write to FPGA. (>= 1.8-18).*
- int [setEnboxType](#) (uint8\_t sel, uint8\_t encType)  
*Sets the EnBOX type (relative or absolute) . Write to FPGA. (>= 1.8-18).*
- int [setEnboxSumDiff](#) (uint8\_t sel, uint8\_t isSumDiff)  
*Set diffOverSum calculations (for EnBOX). 0:Normal the positions are calculated from enc1 and enc2. 1:SumDiff the positions are calculated from enc1+enc2 and enc1-enc2. Write to FPGA. (>= 1.8-18).*
- int [getBPMoffset](#) (double \*offsetX, double \*offsetY, int bpm)  
*Gets BPM offsets in [um] ONLY FOR TETRAMMs. Read BPM offsets from FPGA.*
- int [getBPMoffsetEnbox](#) (double \*offsetX, double \*offsetY, int bpm, int encType)  
*Gets BPM offsets in [um] ONLY FOR Enboxes. Read BPM offsets from FPGA.*
- int [getBPMorient](#) (uint8\_t sel, uint8\_t \*is90deg)  
*Sets BPM orientation for diffOverSum. Read BPM orientation from FPGA.*

- int [getBPMselector](#) (int \*posX, int \*posY, int \*I0)  
*Gets BPM sources. Read BPM sources from FPGA.*
- int [setBPMselector](#) (int posX, int posY, int I0)  
*Sets BPM sources. Write BPM sources to FPGA.*
- int [getCrossBar](#) (uint8\_t sel, uint8\_t \*ch0, uint8\_t \*ch1, uint8\_t \*ch2, uint8\_t \*ch3)  
*Gets input cross switch. Read input cross switch from FPGA.*
- int [setROClimits](#) (int sel, double min, double max)  
*Sets ROC min and max values. ROC should be in [um] for positions and in [A] for intensity. Write min and max ROC levels of specific axis to FPGA.*
- int [getROClimits](#) (int sel, double \*min, double \*max)  
*Gets ROC. ROC values are in [um] for positions and in [A] for intensity. Read min and max ROC levels of specific axis from FPGA.*
- int [getLock](#) (const char \*usr, const char \*pass)  
*Acquires lock on /var/lock/best\_lock.*
- int [getLockStatus](#) ()  
*Gets login level, which can be changed with call on [getLock\(\)](#).*
- unsigned int [getPIDstatus](#) (void)  
*Gets PID status.*
- int [setFBenable](#) (int enable)  
*Enables or disables feedback.*
- int [setCtrlReset](#) ()  
*Resets internal states of controller.*
- int [setPIDconf](#) (char conf)  
*Sets PID controller configuration. Write PID controller configuration to FPGA.*
- int [getPIDconf](#) (char \*conf)  
*Sets PID controller configuration. Read PID configuration from FPGA.*
- int [setSetpoint](#) (char sel, double setpt)  
*Sets new setpoint in [um]. Write setpoint to FPGA. Before writing to FPGA the setpoint is divided by scaling parameter of 1st BPM. Scaling parameters are read from CONFIG\_FILE.*
- int [getSetpoint](#) (char sel, double \*setpt)  
*Gets setpoint in [um]. Read setpoint from FPGA. After reading from FPGA the setpoint is multiplied by scaling parameter. Scaling parameters are read from CONFIG\_FILE.*
- int [setSetpoint2](#) (char sel, double setpt)  
*Sets new setpoint (without using config file). Write setpoint to FPGA. Before writing to FPGA the setpoint is divided by scaling parameter. Scaling parameters are read from FPGA.*
- int [getSetpoint2](#) (char sel, double \*setpt)  
*Gets new setpoint (without using config file). Read setpoint from FPGA. After reading from FPGA the setpoint is multiplied by scaling parameter. Scaling parameters are read from FPGA.*
- int [setSetpointEnbox](#) (char sel, double setpt)  
*Sets new setpoint (without using config file) for Enbox ONLY. Write setpoint to FPGA. Before writing to FPGA the setpoint is NOT divided by scaling parameter. Scaling parameters are read from FPGA.*
- int [getSetpointEnbox](#) (char sel, double \*setpt)  
*Gets new setpoint (without using config file) for Enbox ONLY. Read setpoint from FPGA. After reading from FPGA the setpoint is NOT multiplied by scaling parameter. Scaling parameters are read from FPGA.*
- int [IIRcontrollerSetParam](#) (char ctrlr\_sel, char param\_is\_a, char param\_sel, double param)  
*Sets IIR controller parameter.*
- int [IIRcontrollerCommitParams](#) (char ctrlr\_sel)  
*Commits IIR controller parameters.*
- int [IIRcontrollerGetParam](#) (char ctrlr\_sel, char param\_is\_a, char param\_sel, double \*param)  
*Reads IIR controller parameter.*
- int [funcGenConfig](#) (float freq, float amp[4], float offset[4])  
*Configures function generator.*

- int [funcGenEnable](#) (char enable)  
*Enables or disables function generator.*
- int [tetrammSetRange](#) (int range, unsigned char selector)  
*Sets front-end range.*
- int [tetrammHVSetVoltage](#) (float voltage, unsigned char sel)  
*Sets TetrAMM HV voltage.*
- int [tetrammHVenable](#) (int enable, unsigned char sel)  
*Enables or disables TetrAMM HV module.*
- unsigned int [getTetrAMMsNumber](#) ()  
*Gets number of TetrAMMs.*
- int [getTetrAMMs](#) (int \*pos\_A, int \*pos\_B)  
*Gets TetrAMMs. (>= 1.8-14).*
- int [enboxEncoderSelect](#) (int enc\_type, unsigned char selector)
- unsigned int [getEnboxesNumber](#) ()  
*Gets number of Enbox. (>= 1.8-15).*
- int [getEnboxes](#) (int \*pos\_A, int \*pos\_B)  
*Gets Enbox. (>= 1.8-15).*
- int [getSFPinfo](#) (int selector, uint16\_t \*id, uint32\_t \*timestamp, uint32\_t \*fw\_ver, uint32\_t \*ser\_nr)  
*Gets SFP info.*
- int [getSystemVersion](#) (uint32\_t \*hwVer, uint32\_t \*swVer, uint32\_t \*ctrlVer)  
*Gets System HW, SW and PID versions.*

## Variables

- char [LIB\\_BEST\\_VERSION](#) []

## 7.2.1 Macro Definition Documentation

### 7.2.1.1 BEST\_FILE\_CONFIG\_INI

```
#define BEST_FILE_CONFIG_INI "/opt/CAENels/BEST/config.ini"
```

### 7.2.1.2 BEST\_FILE\_DISP\_DMA

```
#define BEST_FILE_DISP_DMA "/dev/best_dma_displ"
```

### 7.2.1.3 BEST\_FILE\_MAILBOX

```
#define BEST_FILE_MAILBOX "/dev/best_mailbox"
```

### 7.2.1.4 FFT\_LEN

```
#define FFT_LEN (2048)
```

### 7.2.1.5 LEN\_BBF

```
#define LEN_BBF (32)
```

### 7.2.1.6 LOCK\_FILE

```
#define LOCK_FILE "/var/lock/best_lock"
```

## 7.2.2 Variable Documentation

### 7.2.2.1 LIB\_BEST\_VERSION

```
char LIB_BEST_VERSION[ ]
```

## 7.3 best\_c\_interface\_conf.cpp File Reference

```
#include "best_c_interface.h"
#include <stdlib.h>
#include <time.h>
#include <stdint.h>
#include <string.h>
#include <errno.h>
#include <unistd.h>
#include <fcntl.h>
#include <sys/stat.h>
#include <sys/ioctl.h>
#include "pcie_driver/BEST_PCIE.h"
#include "pcie_mailbox/mailbox_comm_defs.h"
#include <iostream>
#include "inih/cpp/INIReader.h"
#include <complex>
#include <fftw3.h>
#include <signal.h>
```

## Functions

- unsigned int [getPIDstatus](#) (void)  
*Gets PID status.*
- int [getPosStats](#) (int selector, double \*meanX, double \*stdX, double \*meanY, double \*stdY, double \*meanI0, double \*stdI0)  
*Get position statistics.*
- int [getBPMscaling\\_sel](#) (int selector, double \*scaleX, double \*scaleY)  
*Gets BPM scaling parameters for position X and Y in [um]. Read values from CONFIG\_FILE.*
- int [getBPMscaling](#) (double \*scaleX, double \*scaleY)  
*Gets BPM scaling parameters of 1st BPM for position X and Y in [um]. Read values from CONFIG\_FILE.*
- int [getBPMscale](#) (int selector, double \*scaleX, double \*scaleY)  
*Gets BPM scaling parameters for position X and Y in [um]. Read BPM scaling parameters from FPGA.*
- int [getROCandROI](#) (double \*roiX, double \*roiY, double \*roiI0min, double \*roiI0max, double \*roc)  
*Gets RoiX, RoiY, RoiI0max, RoiI0min and Roc of 1st BPM. Read values from CONFIG\_FILE.*
- int [getROCEnable](#) (bool \*rocX, bool \*rocY, bool \*rocl0, bool \*rocX2, bool \*rocY2, bool \*rocl02)  
*Gets enables for different ROC checks of 1st nad 2nd BPMs. Read values from FPGA.*
- int [setROCEnable](#) (bool rocX, bool rocY, bool rocl0, bool rocX2, bool rocY2, bool rocl02)  
*Sets enables for different ROC checks of 1st nad 2nd BPMs. Writes values to FPGA.*
- int [setOffsetSingle](#) (enum [best\\_pid\\_select](#) pid\_sel, double offset)  
*Sets output offset in [V] (output=this+PID value, updated even when PID is not running). Write offset to FPGA.*
- int [getOffsetSingle](#) (enum [best\\_pid\\_select](#) pid\_sel, double \*offset)

- Gets output offset in [V]. Read offset from FPGA.*

  - int `setOffset` (double offsetX, double offsetY, double offsetI0)

*Sets output offsets in [V] (output=this+PID value, updated even when PID is not running).*
- int `setWindAvg` (enum `best_pid_select` pid\_sel, uint32\_t nr\_samp)

*Sets number of samples for window averaging.*
- int `getWindAvg` (enum `best_pid_select` pid\_sel, uint32\_t \*nr\_samp)

*Gets number of samples for window averaging. Read number of samples from FPGA.*
- int `setPIDparams` (char pid\_sel, double Kp, double Ki, double Kd, double emin, double lmax, double Omin, double Omax, double Ogain)

*Sets PID parameters (legacy).*
- int `setPIDparamSingle` (char pid\_sel, int pid\_par, double param\_value)

*Sets single PID parameter of specific PID. Write PID parameter to FPGA.*
- int `getPIDparamSingle` (char pid\_sel, int pid\_par, double \*param\_value)

*Gets PID parameters. Read PID parameters from FPGA.*
- int `setPIDOutSel` (uint8\_t posX, uint8\_t posY, uint8\_t I0)

*Sets PID output cross switch. Selects which PID is assigned to which PreDAC output channel. Write PID output cross switch to FPGA.*
- int `getPIDOutSel` (uint8\_t \*posX, uint8\_t \*posY, uint8\_t \*I0)

*Gets PID output cross switch. Read PID output cross switch from FPGA.*
- int `setCrossBar` (uint8\_t sel, uint8\_t ch0, uint8\_t ch1, uint8\_t ch2, uint8\_t ch3)

*Sets input cross switch.*
- int `setBPMorient` (uint8\_t sel, uint8\_t is90deg)

*Sets BPM orientation for diffOverSum.*
- int `getBPMorient` (uint8\_t sel, uint8\_t \*is90deg)

*Sets BPM orientation for diffOverSum. Read BPM orientation from FPGA.*
- int `setOutputMux` (uint8\_t sel)

*Sets value of output mux.*
- int `getOutputMux` (uint8\_t \*sel)

*Gets value of output mux.*
- int `setBPMscale` (double scaleX, double scaleY, int bpm)

*Sets BPM scaling parameters for position X and Y in [um]. Write BPM scaling parameters to FPGA.*
- int `setBPMoffset` (double offsetX, double offsetY, int bpm)

*Sets BPM offset in [um] ONLY FOR TETRAMMs. Write BPM offsets to FPGA.*
- int `setBPMoffsetEnbox` (double offsetX, double offsetY, int bpm, int encType)

*Sets BPM offset in [um] ONLY FOR Enboxes. Write BPM offsets to FPGA. (>= 1.8-16)*
- int `setBPMdevice` (uint8\_t sel, uint8\_t isEnbox)

*Sets BPM device for diffOverSum. Write to FPGA. (>= 1.8-18).*
- int `setEnboxType` (uint8\_t sel, uint8\_t encType)

*Sets the EnBOX type (relative or absolute) . Write to FPGA. (>= 1.8-18).*
- int `setEnboxSumDiff` (uint8\_t sel, uint8\_t isSumDiff)

*Set diffOverSum calculations (for EnBOX). 0:Normal the positions are calculated from enc1 and enc2. 1:SumDiff the positions are calculated from enc1+enc2 and enc1-enc2. Write to FPGA. (>= 1.8-18).*
- int `getBPMoffset` (double \*offsetX, double \*offsetY, int bpm)

*Gets BPM offsets in [um] ONLY FOR TETRAMMs. Read BPM offsets from FPGA.*
- int `getBPMoffsetEnbox` (double \*offsetX, double \*offsetY, int bpm, int encType)

*Gets BPM offsets in [um] ONLY FOR Enboxes. Read BPM offsets from FPGA.*
- int `getBPMselector` (int \*posX, int \*posY, int \*I0)

*Gets BPM sources. Read BPM sources from FPGA.*
- int `setBPMselector` (int posX, int posY, int I0)

*Sets BPM sources. Write BPM sources to FPGA.*
- int `getCrossBar` (uint8\_t sel, uint8\_t \*ch0, uint8\_t \*ch1, uint8\_t \*ch2, uint8\_t \*ch3)



*Gets input cross switch. Read input cross switch from FPGA.*

- int `setROClimits` (int sel, double min, double max)

*Sets ROC min and max values. ROC should be in [um] for positions and in [A] for intensity. Write min and max ROC levels of specific axis to FPGA.*

- int `getROClimits` (int sel, double \*min, double \*max)

*Gets ROC. ROC values are in [um] for positions and in [A] for intensity. Read min and max ROC levels of specific axis from FPGA.*

## Variables

- int `loginLevel`
- int `fd_Mbox`
- int `fd_DMA`
- double \* `in`
- fftw\_complex \* `out`
- fftw\_plan `p`
- int `SAMP_FREQ`

### 7.3.1 Variable Documentation

#### 7.3.1.1 fd\_DMA

```
int fd_DMA
```

#### 7.3.1.2 fd\_Mbox

```
int fd_Mbox
```

#### 7.3.1.3 in

```
double* in
```

#### 7.3.1.4 loginLevel

```
int loginLevel
```

#### 7.3.1.5 out

```
fftw_complex* out
```

#### 7.3.1.6 p

```
fftw_plan p
```

#### 7.3.1.7 SAMP\_FREQ

```
int SAMP_FREQ
```

## 7.4 best\_c\_interface\_data.cpp File Reference

```
#include "best_c_interface.h"
#include <stdlib.h>
#include <time.h>
#include <stdint.h>
#include <string.h>
#include <errno.h>
#include <unistd.h>
#include <fcntl.h>
#include <sys/stat.h>
#include <sys/ioctl.h>
#include "pcie_driver/BEST_PCIE.h"
#include "pcie_mailbox/mailbox_comm_defs.h"
#include <iostream>
#include "inih/cpp/INIReader.h"
#include <complex>
#include <fftw3.h>
#include <signal.h>
```

### Functions

- int [getBuffer](#) (struct [best\\_buffer\\_line](#) buf[], int length)  
*Gets arrays of "samples" from display DMA.*
- int [getPosArray\\_sel](#) (int selector, double \*posX, double \*posY, double \*I0, unsigned int length)  
*Gets arrays of postions and intensity from display DMA.*
- int [getPosArray](#) (double \*posX, double \*posY, double \*I0, unsigned int length)  
*Same as getPosArray\_sel, but only for 1st TetrAMM.*
- int [getVoltageArray\\_sel](#) (int sel, double(\*voltages)[4], unsigned int length)  
*Gets arrays of voltages one of X PreDAC (the PreDAC is only 1 for for the time beign)*
- int [getVoltageArray](#) (double(\*voltages)[4], unsigned int length)  
*Gets arrays of voltages from display DMA.*
- int [getCurrentArray\\_sel](#) (int sel, double(\*currents)[4], unsigned int length)  
*Gets arrays of currents one of two TetrAMMs.*
- int [getCurrentArray](#) (double(\*currents)[4], unsigned int length)  
*Gets arrays of currents from display DMA.*
- double [fftw\\_abs](#) (fftw\_complex f)
- int [getFFT](#) (unsigned int selector, double \*outM, double \*outF, int removeDC)  
*Calculates FFT.*
- int [setDisplayFreq](#) (int freq, uint8\_t use\_filter)  
*Sets display frequency.*
- int [getDisplayFreq](#) (int \*freq, uint8\_t \*use\_filter)  
*Gets display frequency.*
- int [getBPMStats](#) ([best\\_stats](#) \*stats)  
*Gets BPM Statistics.*

### Variables

- int [loginLevel](#)
- int [fd\\_Mbox](#)
- int [fd\\_DMA](#)
- double \* [in](#)
- fftw\_complex \* [out](#)
- fftw\_plan [p](#)
- int [SAMP\\_FREQ](#)

## 7.4.1 Function Documentation

### 7.4.1.1 fftw\_abs()

```
double fftw_abs (
    fftw_complex f )
```

## 7.4.2 Variable Documentation

### 7.4.2.1 fd\_DMA

```
int fd_DMA
```

### 7.4.2.2 fd\_Mbox

```
int fd_Mbox
```

### 7.4.2.3 in

```
double* in
```

### 7.4.2.4 loginLevel

```
int loginLevel
```

### 7.4.2.5 out

```
fftw_complex* out
```

### 7.4.2.6 p

```
fftw_plan p
```

### 7.4.2.7 SAMP\_FREQ

```
int SAMP_FREQ
```

## 7.5 best\_c\_interface\_tetramm.cpp File Reference

```
#include "best_c_interface.h"
#include <stdlib.h>
#include <time.h>
#include <stdint.h>
#include <string.h>
#include <errno.h>
#include <unistd.h>
#include <fcntl.h>
#include <sys/stat.h>
#include <sys/ioctl.h>
```

```
#include "pcie_driver/BEST_PCIE.h"
#include "pcie_mailbox/mailbox_comm_defs.h"
#include <iostream>
#include "inih/cpp/INIReader.h"
#include <complex>
#include <fftw3.h>
#include <signal.h>
```

## Functions

- int [tetrammSetRange](#) (int range, unsigned char selector)  
*Sets front-end range.*
- int [tetrammHVSetVoltage](#) (float voltage, unsigned char sel)  
*Sets TetrAMM HV voltage.*
- int [tetrammHVenable](#) (int enable, unsigned char sel)  
*Enables or disables TetrAMM HV module.*
- unsigned int [getTetrammsNumber](#) ()  
*Gets number of TetrAMMs.*
- unsigned int [getEnboxesNumber](#) ()  
*Gets number of Enbox. (>= 1.8-15).*
- int [getTetramms](#) (int \*pos\_A, int \*pos\_B)  
*Gets TetrAMMs. (>= 1.8-14).*
- int [getEnboxes](#) (int \*pos\_A, int \*pos\_B)  
*Gets Enbox. (>= 1.8-15).*
- int [enboxEncoderSelect](#) (int enc\_type, unsigned char selector)

## Variables

- int [loginLevel](#)
- int [fd\\_Mbox](#)

### 7.5.1 Variable Documentation

#### 7.5.1.1 fd\_Mbox

```
int fd_Mbox
```

#### 7.5.1.2 loginLevel

```
int loginLevel
```

## 7.6 README.md File Reference

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